



5kHz, 400nA, Rail-to-Rail I/O CMOS Operational Amplifier

1 FEATURES

Gain Bandwidth: 5kHz

Rail-to-Rail Input and Output
0.5mV Typical Vos

Input Voltage Range: -0.1V to +5.6V with Vs = 5.5V

• Supply Range: +1.4V to +5.5V

• Specified Up to +125°C

• Micro Size Packages: SOT23-5

2 APPLICATIONS

- Sensors
- Photodiode Amplification
- Wearable Products
- Temperature Measurement
- Battery Powered System

3 DESCRIPTIONS

The RS8021 product offers low voltage operation and rail-to-rail input and output, as well as excellent speed/power consum-ption ratio, providing an excellent bandwidth (5kHz) and slew rate of 1.5V/ms. The op-amp is unity gain stable and feature an ultralow input bias current.

The device is ideal for sensor interfaces, active filters and portable applications. The RS8021 operational amplifier is specified at the full temperature range of -40°C to +125°C under single or dual power supplies of 1.4V to 5.5V.

Device Information (1)

PART NUMBER	PACKAGE	BODY SIZE(NOM)		
RS8021	SOT23-5	2.90mm×1.60mm		

⁽¹⁾ For all available packages, see the orderable addendum at the end of the data sheet.



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4 REVISION HISTORY

Note: Page numbers for previous revisions may different from page numbers in the current version.

Version	Change Date	Change Item
A.1	2024/12/24	Initial version completed



5 PACKAGE/ORDERING INFORMATION (1)

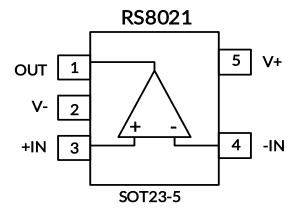
Orderable Device	Package Type	Pin	Channel	Op Temp(°C)	Device Marking ⁽²⁾	MSL (3)	Package Qty
RS8021XF	SOT23-5	5	1	-40°C ~125°C	8021	MSL3	Tape and Reel, 3000

NOTE:

- (1) This information is the most current data available for the designated devices. This data is subject to change without notice and revision of this document. For browser-based versions of this data sheet, refer to the right-hand navigation.
- (2) There may be additional marking, which relates to the lot trace code information (data code and vendor code), the logo or the environmental category on the device.
- (3) RUNIC classify the MSL level with using the common preconditioning setting in our assembly factory conforming to the JEDEC industrial standard J-STD-20F. Please align with RUNIC if your end application is quite critical to the preconditioning setting or if you have special requirement.



6 PIN CONFIGURATION AND FUNCTIONS



PIN DESCRIPTION

	PIN						
NAME	RS8021	I/O ⁽¹⁾	DESCRIPTION				
	SOT23-5						
-IN	4	I	Negative (inverting) input				
+IN	3	I	Positive (noninverting) input				
OUT	1	0	Output				
V-	2	-	Negative (lowest) power supply				
V+	5	-	Positive (highest) power supply				

⁽¹⁾ I = Input, O = Output.



7 SPECIFICATIONS

7.1 Absolute Maximum Ratings

Over operating free-air temperature range (unless otherwise noted) (1)

1 0	1 0 1		MIN	MAX	UNIT	
	Supply, V _S =(V+) - (V-)		7			
Voltage	Signal input pin ⁽²⁾		(V-)-0.5	(V+) +0.5	V	
	Signal output pin ⁽³⁾	(V-)-0.5	(V+) +0.5			
	Signal input pin ⁽²⁾			10	mA	
Current	Signal output pin ⁽³⁾	-55	55	mA		
	Output short-circuit (4)	Cont	Continuous			
θJA	Package thermal impedance (5)	SOT23-5		230	°C/W	
	Operating range, T _A	-40	125			
Temperature	Junction, T _J ⁽⁶⁾	-40	150	°C		
	Storage, T _{stg}		-65	150		

⁽¹⁾ Stresses above these ratings may cause permanent damage. Exposure to absolute maximum conditions for extended periods may degrade device reliability. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those specified is not implied.

- (4) Short-circuit to ground, one amplifier per package.
- (5) The package thermal impedance is calculated in accordance with JESD-51.
- (6) The maximum power dissipation is a function of $T_{J(MAX)}$, $R_{\theta JA}$, and T_A . The maximum allowable power dissipation at any ambient temperature is $P_D = (T_{J(MAX)} T_A) / R_{\theta JA}$. All numbers apply for packages soldered directly onto a PCB.

7.2 ESD Ratings

The following ESD information is provided for handling of ESD-sensitive devices in an ESD protected area only.

		•	VALUE	UNIT
V - FI + + + + + + + + + + + + + + + + + +		Human-Body Model (HBM)	±5000	.,
V _(ESD)	Electrostatic discharge	Machine Model (MM)	±400]



ESD SENSITIVITY CAUTION

ESD damage can range from subtle performance degradation to complete device failure. Precision integrated circuits may be more susceptible to damage because very small parametric changes could cause the device not to meet its published specifications.

7.3 Recommended Operating Conditions

Over operating free-air temperature range (unless otherwise noted)

		MIN	NOM	MAX	UNIT	
Supply voltage, Vs= (V+) - (V-)	Single-supply	1.4		5.5	\/	
	Dual-supply	±0.7		±2.75	v	

⁽²⁾ Input terminals are diode-clamped to the power-supply rails. Input signals that can swing more than 0.5V beyond the supply rails should be current-limited to 10mA or less.

⁽³⁾ Output terminals are diode-clamped to the power-supply rails. Output signals that can swing more than 0.5V beyond the supply rails should be current-limited to ±55mA or less.



7.4 Electrical Characteristics

(At $T_A = +25^{\circ}\text{C}$, $V_S = 5.0\text{V}$, $R_L = 1\text{M}\Omega$ connected to $V_S/2$, and $V_{OUT} = V_S/2$, $\text{Full}^{(9)} = -40^{\circ}\text{C}$ to 125°C , unless otherwise noted.) (1)

	DADAMETED	COMPITIONS	₋	RS8021				
	PARAMETER	CONDITIONS	Tı	MIN ⁽²⁾	TYP ⁽³⁾	MAX ⁽²⁾	UNIT	
POWER	SUPPLY							
Vs	Operating Voltage Range		25°C	1.4		5.5	V	
IQ	Quiescent Current Per Amplifier		25°C		400	1000	nA	
PSRR	Power-Supply Rejection Ratio	V _S =2.5V to 5.5V, V _{CM} =(V-)+0.5V	25°C	62	70		dB	
INPUT								
Vos	Input Offset Voltage	V _{CM} =V _S /2	25°C	-3	±0.5	3	mV	
Vos Tc	Input Offset Voltage Average Drift	V _{CM} =V _S /2	Full		±2.3		μV/°C	
IB	Input Bias Current (4) (5)		25°C	-10	±1	10	pА	
los	Input Offset Current (4)		25°C	-10	±1	10	pА	
V_{CM}	Common-Mode Voltage Range	V _S = 5.5V	25°C	-0.1		5.6	V	
CNADD	C M I D : 1: D !:	V _S = 5.5V, V _{CM} =-0.1V to 4V	25°C	73	90		dB	
CMRR	Common-Mode Rejection Ratio	V _S = 5.5V, V _{CM} =-0.1V to 5.6V	25°C	60	83		dB	
OUTPU	Т							
Δ.	0 1 1/4 6:	V _S =1.4V, R _L =50KΩ, V _O =V _S -0.1V	25°C	85	102		dB	
Aol	Open-Loop Voltage Gain	$V_S=5.0V, R_L=50k\Omega, V_O=V_S-0.1V$	25°C	92	106		dB	
1	Output Swing From Rail	R _L =50ΚΩ	25°C		5		mV	
Іоит	Output Short-Circuit Current (6) (7)		25°C		8		mA	
FREQUI	ENCY RESPONSE		•					
SR	Slew Rate (8)		25°C		1.5		V/ms	
GBP	Gain-Bandwidth Product		25°C		5		kHz	
PM	Phase Margin		25°C		60		0	
NOISE						•		
e _n p-p	Input Voltage Noise	f = 0.1 Hz to 10 Hz	25°C		4.5		μVрр	
en	Input Voltage Noise Density	f = 1 kHz	25°C		360		nV/√Hz	
NOTE:				•				

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⁽¹⁾ Electrical table values apply only for factory testing conditions at the temperature indicated. Factory testing conditions result in very limited self-heating of the device.

⁽²⁾ Limits are 100% production tested at 25°C. Limits over the operating temperature range are ensured through correlations using statistical quality control (SQC) method.

⁽³⁾ Typical values represent the most likely parametric norm as determined at the time of characterization. Actual typical values may vary over time and will also depend on the application and configuration.

⁽⁴⁾ This parameter is ensured by design and/or characterization and is not tested in production.

⁽⁵⁾ Positive current corresponds to current flowing into the device.

⁽⁶⁾ The maximum power dissipation is a function of $T_{J(MAX)}$, $R_{\theta JA}$, and T_A . The maximum allowable power dissipation at any ambient temperature is $P_D = (T_{J(MAX)} - T_A) / R_{\theta JA}$. All numbers apply for packages soldered directly onto a PCB.

⁽⁷⁾ Short circuit test is a momentary test.

⁽⁸⁾ Number specified is the slower of positive and negative slew rates.

⁽⁹⁾ Specified by characterization only.



7.5 Typical Characteristics

NOTE: The graphs and tables provided following this note are a statistical summary based on a limited number of samples and are provided for informational purposes only.

At $T_A = +25$ °C, $V_S=5V$, $R_L = 1M\Omega$ connected to $V_S/2$, $C_L=60$ pF $V_{CM} = V_S/2$, unless otherwise noted.

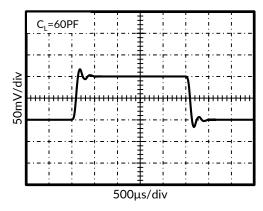


Figure 1. Small-Signal Step Response

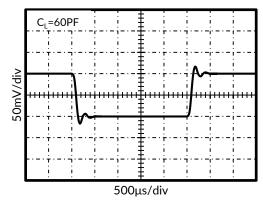


Figure 2. Small-Signal Step Response

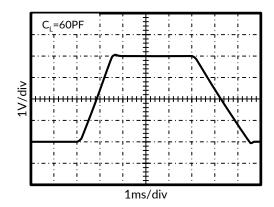


Figure 3. Large-Signal Step Response

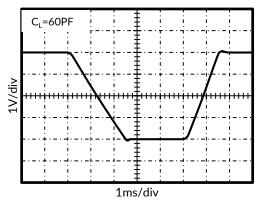


Figure 4. Large-Signal Step Response

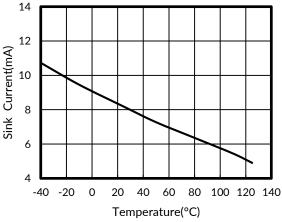


Figure 5. Sink Current vs Temperature

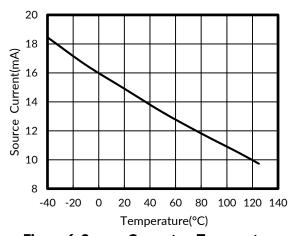
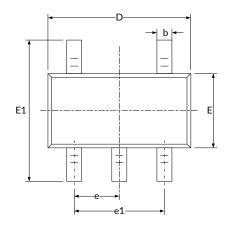


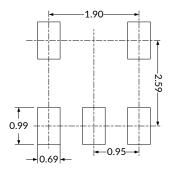
Figure 6. Source Current vs Temperature

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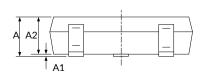


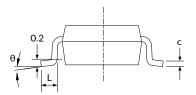
8 PACKAGE OUTLINE DIMENSIONS SOT23-5 (3)





RECOMMENDED LAND PATTERN (Unit: mm)





C. mahal	Dimensions I	n Millimeters	Dimensions In Inches		
Symbol	Min	Max	Min	Max	
A (1)	1.050	1.250	0.041	0.049	
A1	0.000	0.100	0.000	0.004	
A2	1.050	1.150	0.041	0.045	
b	0.300	0.500 0.012		0.020	
С	0.100 0.200		0.004	0.008	
D (1)	2.820	3.020	0.111	0.119	
E (1)	1.500	1.700	0.059	0.067	
E1	2.650	2.950	0.104	0.116	
е	0.950(BSC) (2)	0.037(BSC) (2)	
e1	1.800	2.000	0.071	0.079	
L	0.300	0.600 0.012		0.024	
θ	0°	8°	0°	8°	

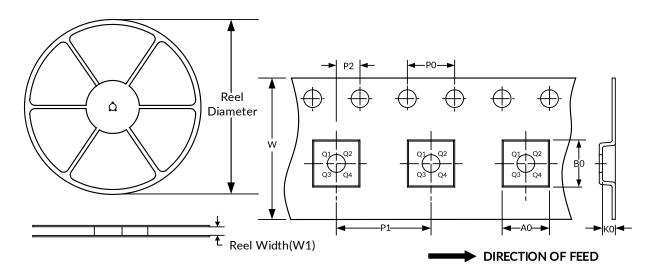
NOTE:

- 1. Plastic or metal protrusions of 0.15mm maximum per side are not included.
- 2. BSC (Basic Spacing between Centers), "Basic" spacing is nominal.
- 3. This drawing is subject to change without notice.



9 TAPE AND REEL INFORMATION REEL DIMENSIONS

TAPE DIMENSION



NOTE: The picture is only for reference. Please make the object as the standard.

KEY PARAMETER LIST OF TAPE AND REEL

Package Type	Reel Diameter	Reel Width (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P0 (mm)	P1 (mm)	P2 (mm)	W (mm)	Pin1 Quadrant
SOT23-5	7"	9.5	3.20	3.20	1.40	4.0	4.0	2.0	8.0	Q3

NOTE:

- 1. All dimensions are nominal.
- 2. Plastic or metal protrusions of 0.15mm maximum per side are not included.



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